

Notice of Allowability

Application No.

09/816,967

Applicant(s)

MANN, GREGORY J.

Examiner

Joshua Joo

Art Unit

2154

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

- This communication is responsive to 1/17/2007.
- The allowed claim(s) is/are 1,2,4-6,8,9,11-13,15,16 and 18-20.
- Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some* c) None of the:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

- A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
- CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
- DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- Notice of References Cited (PTO-892)
- Notice of Draftsperson's Patent Drawing Review (PTO-948)
- Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 3/27/07
- Examiner's Comment Regarding Requirement for Deposit
of Biological Material
- Notice of Informal Patent Application
- Interview Summary (PTO-413),
Paper No./Mail Date 6.
- Examiner's Amendment/Comment
- Examiner's Statement of Reasons for Allowance
- Other _____.

Examiner's Amendment

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given by Attorney Frederick W. Gibb, III on March 27, 2007.

3. The application has been amended as follows:

Claims

1. (Currently amended) A core for providing communications between a transmission media and a processor in a parallel serial architecture, said core comprising:

serial data lanes connecting said processor to said transmission media; and

at least one selector connected to said serial data lanes,

wherein said transmission media and said processor have different operating speeds, and

whereby said selector selectively engages different numbers of said serial data lanes to alter a speed of data passing through said core such that said selector is adapted to perform a speed reduction to accommodate said different operating speeds of said transmission media and said processor[.],

wherein each of said serial data lanes includes a buffer that corrects for frequency deviations in said transmission media and modifies the frequency of signals in said serial data lanes.

3. (Cancelled)

4. (Currently amended) The core in claim [[3]]1, wherein said buffer[[s]] comprise elastic first in, first out (FIFO) buffer[[s]].

8. (Currently amended) A parallel-serial communication system comprising:
at least one processor;
at least one transmission media connecting said at least one processor, wherein said transmission media and said processor have different operating speeds; and
a core between each processor and said transmission media, said core providing communications between said transmission media and said processor, and said core comprising:
serial data lanes connecting said processor to said transmission media; and
at least one selector connected to said serial data lanes,
whereby said selector selectively engages different numbers of said serial data lanes to alter a speed of data passing through said core such that said selector is adapted to perform a speed reduction to accommodate said different operating speeds of said transmission media and said processor[[.]],
wherein each of said serial data lanes includes a buffer that corrects for frequency deviations in said transmission media and modifies the frequency of signals in said serial data lanes.

10. (Cancelled)

11. (Currently amended) The parallel-serial communication system in claim [[10]]8, wherein said buffer[[s]] comprise elastic first in, first out (FIFO) buffer[[s]].

15. (Currently amended) A core for providing communications between a transmission media and a processor in a byte stripped parallel serial InfiniBand architecture, said core comprising:

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serial data lanes connecting said processor to said transmission media; and
at least one selector engages different numbers of said serial data lanes to alter a speed of data
passing through said core such that said selector is adapted to perform a speed reduction to accommodate
said different operating speeds of said transmission media and said processor[.].

wherein each of said serial data lanes includes a buffer that corrects for frequency deviations in
said transmission media and modifies the frequency of signals in said serial data lanes.

17. (Cancelled)

18. (Currently amended) The core in claim [[17]]15, wherein said buffer[[s]] comprise elastic
first in, first out (FIFO) buffer[[s]].

Reasons for Allowance

4. The following is an examiner's statement of reasons for allowance:

None of the prior arts of record teach or fairly suggest all of the limitations recited in the claims.
Prior arts of record does not specifically teach of a core comprising: serial data lanes connecting a
processor to transmission media; and at least one selector connected to the serial data lanes, wherein the
transmission media and the processor have different operating speeds, and whereby the selector
selectively engages different numbers of the serial data lanes to alter a speed of data passing through said
core such that said selector is adapted to perform a speed reduction to accommodate the different
operating speeds of the transmission media and the processor, wherein each of the serial data lanes
includes a buffer that corrects for frequency deviations in said transmission media and modifies the
frequency of signals in the serial data lanes.

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Furthermore, Applicant has agreed to submit a Terminal Disclaimer for the instant application with US Patent #6,665,754 after receiving the examiner's amendment.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance." Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joshua Joo who telephone number is 571 272-3966.

March 28, 2007

JJ

NATHAN J. FLYNN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800